



10/608749

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Grigg et al.

Patent No.: 6,939,501 B2

Issued: September 6, 2005

For: METHODS FOR LABELING
SEMICONDUCTOR DEVICE
COMPONENTS

Attorney Docket No.: 2269-4211.6US

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OFFICE MISTAKES (37 C.F.R. § 1.322)

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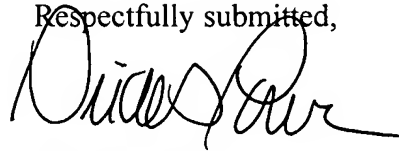
Certificate
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Patent No. 6,939,501 B2

Attached hereto in duplicate is Form PTO/SB/44 with at least one copy being suitable for printing.

Respectfully submitted,



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Date: November 29, 2006
BGP/csw

Enclosures: PTO/SB/44 in duplicate

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,939,501 B2
DATED : September 6, 2005
INVENTOR(S) : Ford B. Grigg, James M. Ocker, and Rick A. Leininger

Page 1 of 1

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In ITEM (56) "References Cited,
Other Publications,"

PAGE 1,	COLUMN 2,	LINE 5,	change "Object" to --Objet--
PAGE 2,	COLUMN 2,	LINE 1,	change "Object" to --Objet--

MAILING ADDRESS OF SENDER:

PATENT NO. 6,939,501 B2

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Salt Lake City, Utah 84102 USA

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